AMENDMENTS TO THE CLAIMS

Please amend the claims as follows:

1.   (Currently Amended)   A system comprising:

a computer system under test including a <u>buffer, a plurality of registers, a</u> |
processor and a memory and configured to boot using a system firmware;

the system firmware including instructions for causing the computer
system to:

detect a test apparatus coupled to the computer system;

<u>store values and information in the registers and the buffer;</u> |

<u>perform a trap operation;</u> |

<u>determine if a test string is detected;</u> |

initiate a manufacturing mode of the system firmware in response
to detecting the test apparatus coupled to the computer system; and

in response to the manufacturing mode being initiated, instructions
in the computer system transfer a control code between the computer
system and the test apparatus.

2.   (Original)   The system of claim 1, wherein the system firmware includes
instructions for causing the computer system to:

provide a first value to the test apparatus;

receive a second value from the test apparatus in response to providing
the first value to the test apparatus; and

initiate the manufacturing mode in response to receiving the second value
from the test apparatus.

3.   (Original)   The system of claim 2, wherein the system firmware includes instructions for causing the computer system to:

store the first value in a first storage location;

store a third value in a second storage location; and

receive the second value from a third storage location identified by the third value.

4.   (Original)   The system of claim 3, wherein the system firmware includes instructions for causing the computer system to:

perform an operation to cause the test apparatus to receive the first value and the third value.

5.   (Original)   The system of claim 4, wherein the operation is an input / output operation.

6.   (Original)   The system of claim 4, wherein the operation is a memory operation.

7.   (Original)   The system of claim 2, wherein the system firmware includes instructions for causing the computer system to:

not initiate the manufacturing mode in response to not receiving the second value from the test apparatus.

8.   (Original)   The system of claim 1, wherein the manufacturing mode of the system firmware includes instructions for causing the computer system to:

receive information from the test apparatus; and

store the information on a device in the computer system.

9.   (Original) The system of claim 1, wherein the manufacturing mode of the system firmware includes instructions for causing the computer system to:

store the system firmware on a device in the computer system.

10. (Currently Amended) A computer program product comprising:

a system firmware processable by a computer system under test for causing the computer system to:

detect a test apparatus coupled to the computer system; and

store values and information in a plurality of registers and a buffer in the system;

perform a trap operation;

determine if a test string is detected;

initiate a manufacturing mode of the system firmware in response to detecting the test apparatus coupled to the computer system;

a storage apparatus from which the system firmware is accessible by the computer system; and

in response to the manufacturing mode being initiated, instructions in the computer system transfer a control code between the computer system and the test apparatus.

11. (Original) The computer program product of claim 10, the system firmware processable by the computer system for causing the computer system to:

provide a first value to the test apparatus;

receive a second value from the test apparatus in response to providing the first value to the test apparatus; and

initiate the manufacturing mode in response to receiving the second value from the test apparatus.

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12. (Original) The computer program product of claim 11, the system firmware processable by the computer system for causing the computer system to:

        store the first value in a first storage location;

        store a third value in a second storage location; and

        receive the second value from a third storage location identified by the third value.

13. (Original) The computer program product of claim 12, the system firmware processable by the computer system for causing the computer system to:

        perform an operation to cause the test apparatus to receive the first value and the third value.

14. (Original) The computer program product of claim 13, wherein the operation is an input / output operation.

15. (Original) The computer program product of claim 13, wherein the operation is a memory operation.

16. (Original) The computer program product of claim 11, the system firmware processable by the computer system for causing the computer system to:

        not initiate the manufacturing mode in response to not receiving the second value from the test apparatus.

17. (Original) The computer program product of claim 10, the manufacturing mode of the system firmware processable by the computer system for causing the computer system to:

        receive information from the test apparatus; and

        store the information on a device in the computer system.

18.   (Original)   The computer program product of claim 10, the manufacturing mode
        of the system firmware processable by the computer system for causing the
        computer system to:
        store the system firmware on a device in the computer system.

19.   (Currently Amended)   A method performed by a computer system comprising:
        providing a computer system under test;
        booting the computer system using a system firmware;
        detecting a test apparatus coupled to the computer system;
        <u>storing values and information in a plurality of registers and a buffer in the
system;</u>
        <u>performing a trap operation;</u>
        <u>determining if a test string is detected;</u>
        initiating a manufacturing mode of the system firmware in response to
        detecting the test apparatus coupled to the computer system; and
        in response to the manufacturing mode being initiated, instructions in the
        computer system transferring a control code between the computer system and
        the test apparatus.

20.   (Original)   The method of claim 19, further comprising:
        providing a first value to the test apparatus;
        receiving a second value from the test apparatus in response to providing
        the first value to the test apparatus; and
        initiating the manufacturing mode in response to receiving the second
        value from the test apparatus.

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21.   (Original)   The method of claim 20, further comprising:

storing the first value in a first storage location;

storing a third value in a second storage location; and

receiving the second value from a third storage location identified by the

third value.

22.   (Original)   The method of claim 21, further comprising:

performing an operation to cause the test apparatus to receive the first

value and the third value.

23.   (Original)   The method of claim 22, further comprising:

performing the operation to cause the test apparatus to receive the first

value and the third value, wherein the operation is an input / output operation.

24.   (Original)   The method of claim 22, further comprising:

performing the operation to cause the test apparatus to receive the first

value and the third value, wherein the operation is a memory operation.

25.   (Original)   The method of claim 20, further comprising:

not initiating the manufacturing mode in response to not receiving the

second value from the test apparatus.

26.   (Original)   The method of claim 19, further comprising:

in response to initiating the manufacturing mode of the system firmware:

receiving information from the test apparatus; and

storing the information on a device in the computer system.

27.   (Original)   The method of claim 19, further comprising:

in response to initiating the manufacturing mode of the system firmware:

storing the system firmware on a device in the computer system.

28.   (Currently Amended)   A system comprising:

a circuit including a system firmware;

a computer system under test coupled to the circuit, configured to boot using the system firmware and configured to provide a first signal to a test apparatus;

the test apparatus configured to provide a second signal to the computer system in response to receiving the first signal;

means for storing values and information in a plurality of registers and a buffer in the system;

means for performing a trap operation;

means for determining if a test string is detected;

the computer system configured to initiate a manufacturing mode of the system firmware in response to receiving the second signal; and

in response to the manufacturing mode being initiated, instructions in the computer system transfer a control code between the computer system and the test apparatus.

29.   (Original)   The system of claim 28, wherein the computer system is configured to store the first signal as a first value in a first storage location, and wherein the test apparatus is configured to store the second signal as a second value in a second storage location.

30.   (Original)   The system of claim 29, wherein the computer system is configured to store a third value in a third storage location, wherein the test apparatus is

configured to receive the third value, and wherein the third value identifies the second storage location.